

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	13	dulac and board and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 16:54
L2	2	benson.in. and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 16:54
L3	0	((control\$5 with board) and (stor\$5 with array with board) and (signal with rout\$5 with board)) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 16:55
L4	0	(distribut\$6 with circuit\$5 with different with board) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 16:56
L5	0	((control\$5 with board) and (stor\$5 with array with board) and (signal with rout\$5 with board)) and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 16:56
S1	0	"6023754".pn. and board	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 16:56
S2	152	dulac and board	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 16:54
S3	1	dulac.in. and board	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 16:58
S4	4	(controller with circuit with board) and (storage with array with circuit with board) and (signal with rout\$5 with circuit with board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 16:58

S5	0	((controller with circuit with board) and (storage with array with circuit with board) and (signal with rout\$5 with circuit with board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 17:00
S6	2	((controller with board) and (storage with array with board) and (signal with rout\$5 with board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 17:00
S7	33	((control\$5 with board) and (stor\$5 with array with board) and (signal with rout\$5 with board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 16:56
S8	237	(distribut\$6 with circuit\$5 with different with board) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 16:55
S9	32	(distribut\$6 near4 circuit\$5 near4 different near4 board) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 12:58
S13	10	(distribut\$6 near4 circuit\$5 near4 different near4 board) and @ad<"20030722" and (power with supply\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/18 12:59